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Title of	
Invention	Ì

MANUFACTURABLE RECESSED STRAINED RSD STRUCTURE AND PROCESS FOR ADVANCED CMOS

Application Number:

Date:

First Named Applicant:

Brian Messenger

Confirmation Number:

Attorney Docket Number:

FIS920040004

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Files Documents being submitted: us-fee-sheet FIS920040004-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd FIS920040004-usidst.xml us-ids us-ids.dtd us-ids.xsl FIS920040004-usrequ.xml us-request us-request.dtd us-request.xsl FIS920040004-usassn.xml us-assignment us-assignment.xsl us-assignment.dtd FIS920040004assign1.tif FIS920040004assign2.tif application-body FIS920040004-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-gname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd FIS920040004dwg1.tif FIS920040004dwg2.tif FIS920040004dwg3.tif FIS920040004dec1.tif us-declaration us-declaration FIS920040004dec2.tif Comments

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